

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Previously Presented) A power control system for a liquid crystal display (LCD) monitor having an LCD panel, the power control system comprising:
 - a light source unit for providing light to the LCD panel;
 - a power supply unit for generating a low direct current (DC) voltage;
 - a high-voltage generator for outputting to the light source unit a high voltage based on the low DC voltage; and
 - a feedback control unit for detecting a voltage induced from the high voltage output, for determining based on a level of the induced voltage an abnormal condition of the high voltage output, and for inhibiting the output of the high-voltage generator during a time corresponding to the abnormal condition by suspending operation of said power supply unit during the time corresponding to the abnormal condition,wherein said feedback control unit comprises a patterned conductor for conducting the induced voltage to said feedback control unit.
2. (Previously Presented) The power control system of claim 1, wherein the abnormal condition is determined when the level of the induced voltage indicates a power surge in the high voltage output.

3. (Canceled)
4. (Previously Presented) The power control system of claim 1, wherein the patterned conductor is a printed current board (PCB) pattern disposed electromagnetically proximate to an electric output of the high-voltage generator and to an electric input of the light source unit.
5. (Currently Amended) The power control system of claim 1, wherein the feedback control unit ~~further comprising~~ comprises:

a power supply control circuit for receiving the induced voltage and for suspending the operation of the power supply unit during the time corresponding to the abnormal condition.
6. (Previously Presented) The power control system of claim 1, wherein the abnormal condition is determined when there is no detection of induced voltage from the high voltage output.
7. (Previously Presented) The power control system of claim 1, wherein the abnormal condition is determined when the level of the induced voltage indicates that the high voltage is less than a predetermined voltage level.

8. (Original) The power control system of claim 5, wherein the power supply control circuit is integrated within the power supply unit.

9. (Original) The power control system of claim 8, wherein the power supply control circuit is a switching mode power supply (SMPS) control circuit.

10. (Currently Amended) The power control system of claim 1, wherein the feedback control unit ~~further comprising~~ comprises:

a coupling capacitor for alternating current (AC) coupling the induced voltage conducted by the patterned conductor;

an integration circuit for integrating the AC-coupled voltage, to output an integrated DC voltage; and

a rectifying diode for outputting the integrated DC voltage, the integrated DC voltage being used to determine the abnormal condition.

11. (Previously Presented) The power control system of claim 10, wherein the integration circuit comprises a series resistor and a shunt capacitor.

12. (Currently Amended) The power control system of claim 5, wherein the feedback control unit ~~further comprising~~ comprises:

a Zener diode for limiting the induced voltage received by the power supply control circuit.

13. (Original) The power control system of claim 1, wherein the light source unit is a fluorescent lamp.

14. (Previously Presented) The power control system of claim 13, wherein the fluorescent lamp is a cold cathode fluorescent lamp (CCFL).

15. (Currently Amended) A power control system for a liquid crystal display (LCD) monitor having a LCD panel, the power control system comprising:

a light source unit for providing light to the LCD panel;

a power supply unit for generating a standard DC voltage;

a direct current to direct current (DC/DC) converter for converting the standard DC voltage to a predetermined DC voltage;

a high-voltage generator for outputting to the light source unit a high voltage based on the predetermined DC voltage; and

a feedback control unit for detecting a voltage induced from the high voltage output, for determining based on a level of the induced voltage an abnormal condition of the

high voltage output, and for inhibiting the output of the ~~high-voltage-high-voltage~~ generator during a time corresponding to the abnormal condition by suspending operation of said power supply unit during the time corresponding to the abnormal condition,

wherein said feedback control unit comprises a printed circuit board (PCB) pattern disposed electromagnetically proximate to an electrical output of the high-voltage generator and to an electrical input of the light source unit, for conducting the induced voltage to said feedback control unit.

16. (Currently Amended) The power control system of claim 15, wherein the feedback control unit ~~further comprising~~ comprises:

a power supply control circuit for receiving the induced voltage and for suspending the operation of the power supply unit during the time corresponding to the abnormal condition.

17. (Original) The power control system of claim 16, wherein the power supply control circuit is integrated within the power supply unit.

18. (Original) The power control system of claim 17, wherein the power supply control circuit is a switching mode power supply (SMPS) control circuit.

19. (Currently Amended) The power control system of claim 16, wherein the feedback control unit ~~further comprising~~ comprises:

a coupling capacitor alternating current (AC) coupling the induced voltage conducted by the PCB pattern;

an integration circuit for integrating the AC-coupled voltage, to output an integrated DC voltage; and

a rectifying diode for outputting the integrated DC voltage to the power supply control circuit,

wherein the power supply control circuit uses the integrated DC voltage to determine the abnormal condition.

20. (Previously Presented) The power control system of claim 19, wherein the integration circuit comprises a series resistor and a shunt capacitor.

21. (Currently Amended) The power control system of claim 16, wherein the feedback control unit ~~further comprising~~ comprises:

a Zener diode for limiting the induced voltage received by the power supply control circuit.

22. (Previously Presented) The power control system of claim 15, further comprising:
a main controller for generating a power control signal for controlling an on/off function of the power supply unit and for generating a brightness control signal for controlling brightness of the light source unit.

23. (Previously Presented) The power control system of claim 22, further comprising:
a brightness controller for controlling the predetermined DC voltage output from the DC/DC converter according to the brightness control signal received from the main controller.

24. (Previously Presented) The power control system of claim 1, wherein the patterned conductor is configured to transmit the conductor voltage, the transmitted voltage representing the abnormal condition if a power surge exists in the high voltage output, representing the abnormal condition if there is no detection of induced voltage from the high voltage output, and representing the abnormal condition if the level of the induced voltage indicates that the high voltage is less than a predetermined voltage level.

25. (Previously Presented) The power control system of claim 1, wherein the patterned conductor is electrically isolated from the high voltage output and has two ends, one end being connected to an input of the feedback control unit and the other end being grounded.

26. (Previously Presented) A method for controlling power to a liquid crystal display (LCD) monitor having an LCD panel, the method comprising:

- generating a low direct current (DC) voltage;
- generating a high voltage based on the low DC voltage;
- applying the generated high voltage to a light source unit for providing light to the LCD panel;
- detecting a voltage induced from the high voltage applied to the light source unit;
- determining, based on a level of the induced voltage, an abnormal condition of the high voltage output; and
- inhibiting said high-voltage generation during a time corresponding to the abnormal condition by suspending said low DC voltage generating.

27. (Currently Amended) The method of claim 26, wherein the detected voltage is induced into a patterned conductor for conducting the induced voltage to ~~the~~ a feedback control unit and wherein the patterned conductor is configured to transmit the conducted voltage, the transmitted voltage representing the abnormal condition if a power surge exists in the high voltage output, representing the abnormal condition if there is no detection of induced voltage from the high voltage output, and representing the abnormal condition if the induced voltage indicates that the high voltage is less than a predetermined voltage level.